

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)[Cases](#)**Search Results -**

Terms	Documents
L8 and integrator	1

Database:

- US Patents Full-Text Database
- US Pre-Grant Publication Full-Text Database
- JPO Abstracts Database
- EPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

09 / 727,713

Examiner: T. R. Sundaram

Search:

L11

[Refine Search](#)[Recall Text](#)[Clear](#)**Search History****DATE:** Wednesday, March 12, 2003 [Printable Copy](#) [Create Case](#)

Set Name Query
side by side

Hit Count Set Name
result set

DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ

<u>L11</u>	L8 and integrator	1	<u>L11</u>
<u>L10</u>	L9 and integrator	0	<u>L10</u>
<u>L9</u>	L8 and comparator	3	<u>L9</u>
<u>L8</u>	L3 and pulse	15	<u>L8</u>
<u>L7</u>	L3 and pulse and differentiat\$3	0	<u>L7</u>
<u>L6</u>	L3 and pulse with differentiat\$3	0	<u>L6</u>
<u>L5</u>	L3 and ((324/\$)!.CCLS.)	0	<u>L5</u>
<u>L4</u>	L3 and ((324/\$)!.CCLS.)	0	<u>L4</u>
<u>L3</u>	L2 and MOSFET and switch	18	<u>L3</u>
<u>L2</u>	L1 and synchronous with rectifier	40	<u>L2</u>
<u>L1</u>	AC/DC with conver\$4 and transformer	716	<u>L1</u>

END OF SEARCH HISTORY

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Terms	Documents
L16 and gate with synchronous ADJ rectfier	0

Database:

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Search:

L19

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Set Name Query

side by side

DB=USPT; THES=ASSIGNEE; PLUR=YES; OP=ADJ

<u>L19</u>	L16 and gate with synchronous ADJ rectifier		
<u>L18</u>	L16 and MOSFET with synchronous ADJ rectifier	0	<u>L18</u>
<u>L17</u>	L16 and MOSFET	8	<u>L17</u>
<u>L16</u>	L15 and switch\$3	10	<u>L16</u>
<u>L15</u>	L14 and synchronous ADJ rectifier	10	<u>L15</u>
<u>L14</u>	L13 and output\$3 near DC	209	<u>L14</u>
<u>L13</u>	L12 and transformer	500	<u>L13</u>
<u>L12</u>	AC/DC ADJ converter	1199	<u>L12</u>
<u>L11</u>	L8 and integrator	1	<u>L11</u>
<u>L10</u>	L9 and integrator	0	<u>L10</u>
<u>L9</u>	L8 and comparator	3	<u>L9</u>
<u>L8</u>	L3 and pulse	15	<u>L8</u>
<u>L7</u>	L3 and pulse and differentiat\$3	0	<u>L7</u>
<u>L6</u>	L3 and pulse with differentiat\$3	0	<u>L6</u>
<u>L5</u>	L3 and ((324/\$)!.CCLS.)	0	<u>L5</u>
<u>L4</u>	L3 and ((324/\$)!.CCLS.)	0	<u>L4</u>
<u>L3</u>	L2 and MOSFET and switch	18	<u>L3</u>
<u>L2</u>	L1 and synchronous with rectifier	40	<u>L2</u>
<u>L1</u>	AC/DC with conver\$4 and transformer	716	<u>L1</u>

END OF SEARCH HISTORY